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Subclass	ISSUE CLASSIFICATION
Class	

PATENT NUMBER

U.S. **UTILITY** Patent Application

O.I.P.E. SCANNED ____ Q.A. ____	PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/853005	F	711	118	2185 2186 2187	~~M. Kim~~ Baker

APPLICANTS

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TITLE

Peripheral device interface chip cache and data synchronization method

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION			
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☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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